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APPLICANT:


Prieto et al.

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
U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A	5,889,779	3/30/99	Lincoln			
	B	5,974,465	10/26/99	Wong			
	C						
	D						
	E						
	F						
	G						
	H						
	I						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	J							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	AA	IEEE Communications Magazine, "Hardware Implementation of Fair Queuing Algorithms for Asynchronous Transfer Mode Networks", Varma and Stiliadis, Pg. 54 et seq (December 1999).
	BB	Proceedings of the IEEE, "Service Disciplines for Guaranteed Performance Service in Packet-Switching Networks", Zang, Vol. 83, No. 10, Pg. 1374 et seq (October 1999).
	CC	Proceedings of the 1997 International Conference on Networking Protocols, "High Speed, Scalable, and Accurate Implementation of Packet Fair Queuing Algorithms in ATM Networks", Bennett, Stephens and Zhang, Pg. 7-14, (October 1997).
	DD	

EXAMINER

DATE CONSIDERED

12/11/03

*EXAMINER: Initial citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.